

Amendments to the Specification:

Please amend the following paragraph beginning on page 15, line 33:

The pixel electrode 52 is formed in the pixel area and extends into each edge of all sides of the storage electrode 60 [[except]] in addition to the area of the storage electrode 60, as shown in Fig. 11. Consequently, the lower portion of the pixel electrode 52 is not provided with the active layer (not shown) and the storage electrode 60. Thus, the pixel electrode 52 is uniformly deposited onto the gate insulating film 42. A storage capacitor is formed with the storage electrode 60 and the gate line 32 having the gate insulating film 42 in between them.